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(CORRECTIONS ARE MADE IN BOLD TYPE)

Inventor Information

Inventor One Given Name::	Frederic
Family Name::	GAILLARD
Postal Address Line One::	Rue des Tallifardieres
City::	Voiron
State or Province::	
Postal or Zip Code::	F-38500
Citizenship Country::	France
Inventor Two Given Name::	Fabrice
Family Name::	GEIGER
Postal Address Line One::	29 Avenue de la Pleine Fleurie
City::	Meylan
State or Province::	
Postal or Zip Code::	F-38240
Citizenship Country::	France
Inventor Three Given Name::	Ellie
Family Name::	YIEH
Postal Address Line One::	5888 Pistoria Way
City::	San Jose
State or Province::	California
Postal or Zip Code::	95138
Citizenship Country::	United States of America

Correspondence Information

Correspondence Customer Number::	20350
Correspondence Address:	<b>Applied Materials, Inc.</b> <b>Legal Affairs Department</b> <b>P.O. Box 450A</b> <b>Santa Clara, California 95052</b>

Application Information

Title Line One::	INTEGRATION SCHEME USING SELF-PLANARIZED
Title Line Two::	DIELECTRIC LAYER FOR SHALLOW TRENCH ISOLATION
Formal Drawings?::	No
Application Type::	Utility
Docket Number::	<b>AM3299/T29900</b>
Secrecy Order in Patent Appl.?::	No

Prior Foreign Applications

Foreign Application One:: PCT/US00/22561  
Filing Date:: 16 August 2000  
Country:: PCT  
Priority Claimed:: Yes

Foreign Application Two:: EP99402068.3  
Filing Date:: 16 August 1999  
Country:: Europe  
Priority Claimed:: Yes